



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application Of:

Dvorak, et al.

Application No.: 09/981,603

Filed: November, 2004

For: Arc Fault Circuit Interrupter System

) Atty. Docket No.: 47181-00259

) Examiner: Boris Benenson

) Group Art Unit: 2836

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, postage prepaid, in an envelope addressed to the Assistant Commissioner for Patents, Attn: Box Non-Fee Amendment, Washington, D.C. 20231, on:

Date:

Nov 23, 2004

Signature:

Therese Kizal

37 C.F.R. § 1.131 DECLARATION
OF ROBERT F. DVORAK

Assistant Commissioner for Patents
Alexandria, VA 22313-1450

Attn: Box Non-Fee Amendment

I, ROBERT F. DVORAK declare:

1. I am a co-inventor, with Kon B. Wong, of the subject matter described and claimed in the above-identified patent application, Application No. 09/981,603 ("the present application").

2. This declaration is to establish completion of the invention in the present application in the United States, at a date prior to July 10, 2001, the effective date of U.S. Patent No. 6,259,996, which was cited by the Examiner.

3. The document attached hereto as Exhibit 1 is a true photocopy of an invention disclosure statement dated prior to July 10, 2001 (date redacted) showing completion of the above-identified application in the United States of America prior to July 10, 2001.

4. Kon B. Wong and I diligently worked on the arc fault circuit interrupter system from at least a time prior to July 10, 2001 until the completion of the invention.

5. I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the above-referenced application or any patent issuing thereon.

Nov 22, 2004
DATE

Robert F. Dvorak
ROBERT F. DVORAK